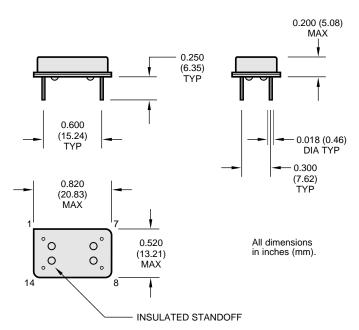
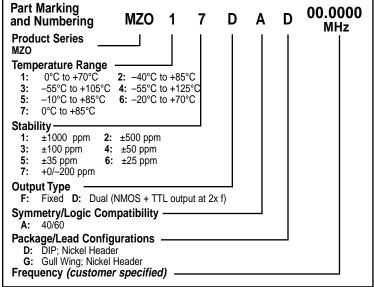
MZO Series NMOS Oscillators



MZO Series NMOS Clock Oscillators With Optional TTL Auxiliary Outputs





Available Stabilities vs Temperature

TS	1	2	3	4	5	6	7
1	Α	Α	Α	Α	Α	Α	S
2	Α	Α	Α	Α	Α	Α	Α
3	Α	Α	Α	Α	N	N	Α
4	Α	Α	Α	Α	N	N	Α
5	Α	Α	Α	Α	Α	Α	Α
6	Α	Α	Α	Α	Α	Α	Α
7	Α	Α	Α	Α	Α	Α	Α

A = AVAILABLE S = STANDARDN = NOT AVAILABLE

Pin Connections

PIN	FUNCTION				
1	N/C or optional TTL output				
7	Circuit/Case Ground				
8	NMOS Output				
14	+V _{cc}				

Electrical Specifications

(Standard Operating Conditions 0°C to 70°C; V_{CC} = 5.0 ±10% V DC)

	NMOS Load Pin 8 NMOS						
			Pin 1 TTL		Pin 8 NMOS		
PARAMETERS	MIN.	MAX.	MIN.	MAX.	MIN.	MAX.	UNITS
Frequency Range	1.75	12.0	3.5	12.0	1.75	6.0	MHz
Output Load		150 ^{pF2}		10 TTL ¹		150 pF ²	pF/TTL
Symmetry *	40/60	60/40	40/60	60/40	45/55	55/45	%
Logic "0" Level		0.45		0.5		0.45	V
Logic "1" Level	V _{cc} - 0.4		2.4		V _{cc} -0.4	ļ	V
Rise/Fall Time**		20		10		20	ns
Supply Current		100		110		110	mA

¹⁻ See load circuit #1 on page 63. 2- See load circuit #3 on page 63.

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^{*}Symmetry is measured at 1.4 V with TTL load, and at $V_{dd}/2$ with HCMOS load.

^{**}Rise/fall times are measured between 0.5 V and 2.4 V with TTL load, and between 10% V_{dd} and 90% V_{dd} with HCMOS load.